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What is claimed is;

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1. An image-capturing device comprising:  
a plurality of photoelectric conversion elements  
that are two-dimensionally arrayed;  
5 a charge transfer circuit that transfers electrical  
charges from said photoelectric conversion elements; and  
an amplifier that is connected to an end of said  
charge transfer circuit along a direction of charge  
transfer, converts an electrical charge into voltage, and  
10 amplifies the voltage, wherein:  
at least said photoelectric conversion elements,  
said charge transfer circuit and said amplifier are  
provided on a single semiconductor substrate; and  
15 said image-capturing device further comprises an  
amplifier power control circuit that controls power to  
said amplifier in conformance to a control signal  
provided from outside.
  2. An image-capturing device according to claim 1,  
20 wherein:  
said amplifier power control circuit changes a bias  
current supplied to said amplifier by using said control  
signal.
  - 25 3. An image-capturing device according to claim 1,

wherein:

said charge transfer circuit transfers the electrical charges to said amplifier by employing a CCD (charge-coupled device).

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4. An image-capturing device according to claim 1, wherein:

said charge transfer circuit reads the electrical charges out to said amplifier through XY address scanning.

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5. An electronic camera comprising:

an image-capturing device that captures an image of a subject and outputs image data; and

a control device that performs a specific type of image processing on the image data, wherein said image-capturing device comprises:

a plurality of photoelectric conversion elements that are two-dimensionally arrayed;

a charge transfer circuit that transfers electrical charges from said photoelectric conversion elements; and

an amplifier that is connected to an end of said charge transfer circuit along a direction of charge transfer, converts an electrical charge into voltage, and amplifies the voltage, wherein:

at least said photoelectric conversion elements,

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said charge transfer circuit and said amplifier are provided on a single semiconductor substrate; and

said image-capturing device further comprises an amplifier power control circuit that controls power to  
5 said amplifier in conformance to a control signal  $\#$  11 provided from outside.

6. An electronic camera according to claim 5, wherein:  
said control device controls said image-capturing  
10 device so that a normal bias current is supplied to said amplifier in conformance to said control signal when discharging unnecessary electrical charges and reading out electrical charges from said photoelectric conversion elements, and the bias current to said amplifier is  
15 reduced in conformance to said control signal at other times.

7. An electronic camera according to claim 6, wherein:  
said control device controls said image-capturing  
20 device so that the normal bias current is supplied to said amplifier in conformance to said control signal when exposure is performed over a length of time equal to or less than a specific length of time.

25 8. An image-capturing device comprising:

